

IFW
2112**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:
Richard A. Zatorski

Serial No.: 09/912,745

Filed: July 24, 2001

For: **BUS BRIDGE CIRCUIT
INCLUDING AUDIO LOGIC
ACCESES DIGITAL DATA,
AND METHOD FOR
INITIALIZING A CHIP SET
INCLUDING THE BUS BRIDGE
CIRCUIT**

§ Group Art Unit: 2181
§
§ Examiner: Unknown
§
§ Atty Dkt.: 5681-46700
§

§ I hereby certify that this correspondence is being deposited with the
§ U.S. Postal Service as First Class Mail in an envelope addressed to
§ Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-
§ 1450, on the date indicated below:

§ B. Noël Kivlin
§ Name of Registered Representative

§ 7/29/04
§ Date

§ 
§ Signature

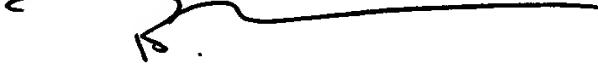
SUBMISSION OF FORMAL DRAWINGS

Official Draftsman
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant hereby submits the formal drawings for the above-referenced application and requests that these drawings be accepted for filing.

Respectfully submitted,



B. Noël Kivlin
Reg. No. 33,929

Meyertons, Hood, Kivlin,
Kowert & Goetzl, P.C.
P.O. Box 398
Austin, TX 78767-0398
(512) 853-8800

Date: 7/29/04